

## Course Information

September 2, 2011

### ■ Course Goal

- This course introduces basic components of modern digital logic design and covers important concepts and theories necessary in building modern digital hardware systems using basic components. The course focuses on the combinational and sequential logic, their main functions and building blocks, high-level design specifications and simulations, and design optimization techniques.

### ■ Instructor and TA

- Instructor: Jihong Kim 김지홍 (office: Room 328 @Building 302)  
Email: [jihong@davinci.snu.ac.kr](mailto:jihong@davinci.snu.ac.kr) Phone: 880-8792  
Office hours: Wednesdays 17:00 – 18:00 (or by appointment)
- TAs: Sangwook Hahn 한상욱 (315-2 @302, shanehahn@davinci.snu.ac.kr, 880-1861)  
Office hours: Wednesdays 16:00 – 17:00 (or by appointment)

### ■ Class Hours & Course Homepage

- Mondays & Wednesdays 14:00 – 15:15 @302-208
- Course homepage: [http://davinci.snu.ac.kr/courses/logic/2013\\_2](http://davinci.snu.ac.kr/courses/logic/2013_2)
  - Important notices regarding the course will be announced in the course homepage. Please visit the course homepage regularly.
  - Lecture slides will be available before the lecture at the homepage.

### ■ Prerequisite

- Programming experience

### ■ Textbook

- Randy H. Katz and Gaetano Borriello  
*Contemporary Logic Design, 2<sup>nd</sup> Edition*  
Pearson Prentice Hall

■ **Grading**

- Midterm: 25% (mid October)
- Final: 35% (early December)
- Assignments: 30%
- Quizzes: 5%
- Attendance: 5%
- **Course Repeat Policy: the highest grade is limited to A-.**

■ **Course Outline (Tentative)**

We will cover Chapter 1 through Chapter 10 of the textbook.

■ **Assignments (Tentative)**

- Five written/design assignments

■ **Assignment Submission Policy**

- All the assignments **SHOULD** be turned in before the due date. Late submissions are not accepted under normal circumstances. (Even when late submissions are allowed, a penalty will be deducted. Usually, 20% of the total assignment point is deducted as a penalty.)

■ **Cheating Policy**

Any type of cheating (e.g., copying others' assignment/programs, stealing an examination), if found, a grade of F will be assigned. For a further disciplinary action, the College of Engineering will be notified of the cheating activity.